

The proper size "AMOBEBADS" core is selected by calculating the necessary voltage times the time in seconds (=flux). From its operating theory, there is a need to increase the voltage used in the calculation by that which develops during the reverse recovery period of diode. The multiple of the voltage and time (voltage times second) is equal to the operating flux. Therefore, the magnetization $\Delta \phi_{ns}$ necessary to suppress the noise is calculated by the voltage $E_c[V]$ and time for reverse recovery of diode, that is added to "AMOBEBADS"

$$\Delta \phi_{ns} [Wb] = E_c \times t_{rr} [V \times Sec]$$

A good result is achieved when the voltage E_c added to "AMOBEBADS" is close to voltage added to diode. Please select the "AMOBEBADS" that have a larger core magnetization ϕ_c than the voltage times seconds that was calculated here. However, the actual noise suppression result for "AMOBEBADS" on real circuit may differ from the calculated value due to the peculiar recovery characteristics of the diode used or the circuit structure. So please confirm the effect by performing examination. "AMOBEBADS" can be also affected by things like a CR snubber, so please perform evaluation under condition without any effect of a snubber.

Since "AMOBEBADS" have high circuit voltage, sometimes an insufficient result is obtained when the reverse recovery time is long and has minimal magnetization. Under this condition, please consider a wire wound type "SPLIKE KILLER"

Example of "AMOBEBADS" Selection

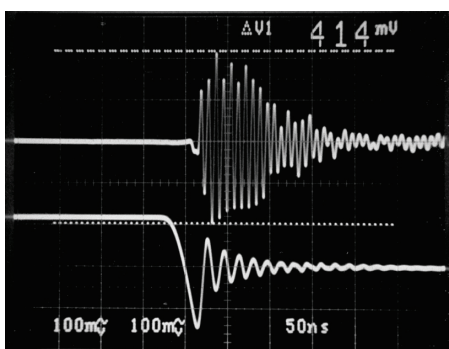
Forward Converter

t _{rr}	Output Voltage				
	3.3V	5V	12V	15V	24V
35nsec	AB3×2×3W	AB3×2×4.5W	AB3×2×6W	AB4×2×4.5W	AB4×2×6W
60nsec	AB3×2×4.5W	AB3×2×6W	AB4×2×4.5W	AB4×2×6W	SPIKE KILLER

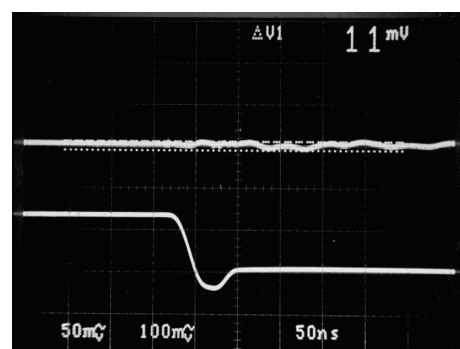
Flyback Converter

t _{rr}	Output Voltage				
	3.3V	5V	12V	15V	24V
35nsec	AB3×2×3W	AB3×2×3W	AB3×2×4.5W	AB3×2×6W	AB4×2×4.5W
60nsec	AB3×2×3W	AB3×2×4.5W	AB3×2×6W	AB4×2×4.5W	AB4×2×6W

Example of Noise Reduction



Without Countermeasure



With AMOBEBADS
(AB4×2×8W)

Principle of the Noise Suppressing Device

We will explain the behavior of "AMOBEDS" when slipped over the lead of a switching power supply output diode.

Period I (When Diode is On)

During period I, which is when the diode is in the "ON" condition and the forward current is running, the "AMOBEDS" are in the saturated magnetic condition "I". There will be almost no inductance under this condition. (Inductance is proportional to the slope of the B-H curve.)

Period II (When Diode is Turn Off)

During period II, which is when the diode current starts to turn off and the current decreases heading towards zero, the "AMOBEDS" magnetization curve will change like "II" in a condition of almost no inductance until the current crosses zero. Since there is no inductance during this period II, the angle or slope of the diode current during turn off is constant, a unique characteristic of the "AMOBEDS". If materials such as ferrite is used, inductance will occur during this period II and the angle or slope of current during the turn off period will change and this will lead to increased diode loss.

Period III (Reverse Recovery Period)

During period III, a reverse recovery current tries to flow in a direction opposite to the normal direction of current flow of the diode and as a result, the magnetization curve of the "AMOBEDS" change like "III" and the inductance increases rapidly. At this time, the large inductance of the "AMOBEDS" intercepts and opposes the recovery current and converts the current into a soft recovery condition. Thus by converting the sharp reverse recovery to a soft recovery condition by decreasing the rate of the current change (di/dt), the "AMOBEDS" minimize the rapid change of current (High di/dt) and suppress the noise in the circuit.

Period IV (After Reverse Recovery Ends)

During period IV, when the reverse recovery of the diode ends, the magnetization of the "AMOBEDS" will move parallel to the vertical axis of the magnetization curve as shown in period "IV".

Period V (When Diode is Turn On)

The "AMOBEDS" magnetization will change as shown in "V" of the magnetization curve and go back to a saturation condition. At this point, the diode will turn on and after a slight delay of the start up of current, the next current pulse will develop and the cycle described above from Period I thru V will repeat itself.

As the complete cycle repeats itself at the circuit operating frequency, the "AMOBEDS" repeatedly suppress circuit noise during period III of the cycle by eliminating the rapid change in the reverse recovery current of the diode, which is the cause of noise.

"AMOBEDS" use a cobalt based amorphous alloy with a small coercive force under frequency and this results in excellent noise suppression.

